

Memory Ten PCB Part Number Decoder

MT_512M8D3216_REVA

Board Revision
Chip Geometry

- 44 = 4M X 4
- 2564 = 256M X 4
- 1288 = 128M X 8
- 6416 = 64M X 16
- 1284 = 128M X 4
- 648 = 64M X 8
- 3216 = 32M X 16
- 644 + 64M X 4
- 328 = 32M X 8
- 1616 = 16M X 16
- 324 = 32M X 4
- 168 = 16M X 8
- 816 = 8M x 16
- 164 = 16M X 4
- 88 = 8M X 8
- 416 = 4M X 16

Device Type

- E = EDO
- F = Fast Page Mode
- S = SDRAM
- T = DDR2
- Y = DDR3

Number of chips on fully populated board

Module Configuration

- B = 168 Pin 5V Buffered DIMM
- C = 30 Pin SIMM
- D = 184 Pin Unbuffered DIMM
- E = 184 Pin Registered DIMM
- F = 200 Pin DDR1/2 SO DIMM
- H = 100 Pin SO DIMM
- I = 144 Pin Micro DIMM
- M = 172 Pin Micro DIMM
- N = 240 Pin DDR2 Unbuffered DIMM
- S = 144 Pin SO DIMM/144 Pin DDR2 SODIMM
- T = 72 Pin SIMM
- U = 168 Pin Unbuffered DIMM
- V = 168 Pin 3.3V Buffered DIMM
- Q = 200 Pin Sun DIMM

Max. Capacity of fully populated Module

Memory Ten Product